

	Type	Hits	Search Text	DBs
1	BRS	1	"20010008533"	USPAT; US-PGPUB; EPO; JPO
2	BRS	1506	370/468.ccls.	USPAT; US-PGPUB; EPO; JPO
3	BRS	1133	370/468.ccls. and @ad<20010117	USPAT; US-PGPUB; EPO; JPO
4	BRS	2	(370/468.ccls. and @ad<20010117) and (delay\$3 near compensat\$3 with (timeslot (time adj slot)))	USPAT; US-PGPUB; EPO; JPO
5	BRS	3	370/468.ccls. and (delay\$3 near compensat\$3 with (timeslot (time adj slot)))	USPAT; US-PGPUB; EPO; JPO
6	BRS	1	jp-09214459-.did.	USPAT; US-PGPUB; EPO; JPO
7	BRS	1	jp-58218250-.did.	USPAT; US-PGPUB; EPO; JPO
8	BRS	1	jp-10242981-.did.	USPAT; US-PGPUB; EPO; JPO
9	BRS	16	timeslot and queue and (delay-compensat\$3 (delay adj compensat\$3) (delay\$3 near compensat\$3))	USPAT; US-PGPUB; EPO; JPO
10	BRS	9	(timeslot and queue and (delay-compensat\$3 (delay adj compensat\$3) (delay\$3 near compensat\$3))) and @ad<20010117	USPAT; US-PGPUB; EPO; JPO
11	BRS	544	delay\$3 with compensat\$3.ti.	USPAT; US-PGPUB; EPO; JPO
12	BRS	5	delay\$3 with compensat\$3 with timeslot.ti.	USPAT; US-PGPUB; EPO; JPO
13	BRS	407	(delay\$3 with compensat\$3.ti.) and @ad<20010117	USPAT; US-PGPUB; EPO; JPO
14	BRS	3	(delay\$3 with compensat\$3 with timeslot.ti.) and @ad<20010117	USPAT; US-PGPUB; EPO; JPO
15	BRS	2	((delay\$3 with compensat\$3.ti.) and @ad<20010117) and timeslot and packet and buffer	USPAT; US-PGPUB; EPO; JPO
16	BRS	4070	370/465, 468,470,473,498,499,516-522,532,537,538. ccls.	USPAT; US-PGPUB; EPO; JPO
17	BRS	3626	(370/465, 468,470,473,498,499,516-522,532,537,538. ccls.) and @ad<20010117	USPAT; US-PGPUB; EPO; JPO
18	BRS	43	(((370/465, 468,470,473,498,499,516-522,532,537,538. ccls.) and @ad<20010117) and delay and (timeslot (time near slot))) and (point-to-multipoint (point near (multi-point multipoint (multi adj point)))))	USPAT; US-PGPUB; EPO; JPO

	Time Stamp	Comments	Error Definition	Errors
1	2004/05/20 13:57			0
2	2004/05/23 17:49			0
3	2004/05/23 17:51			0
4	2004/05/23 17:51			0
5	2004/05/23 17:51			0
6	2004/05/23 17:55			0
7	2004/05/23 17:56			0
8	2004/05/23 17:56			0
9	2004/05/24 14:37			0
10	2004/05/24 17:09			0
11	2004/05/24 21:42			0
12	2004/05/24 21:42			0
13	2004/05/24 21:43			0
14	2004/05/24 21:45			0
15	2004/05/24 21:45			0
16	2004/05/25 14:35			0
17	2004/05/25 15:10			0
18	2004/05/25 14:38			0

	Type	Hits	Search Text	DBs
19	BRS	27	((((370/465, 468,470,473,498,499,516-522,532,537,538. ccls.) and @ad<20010117) and delay and (timeslot (time near slot))) and (point-to-multipoint (point near (multi-point multipoint (multi adj point)))))) and TDM\$1	USPAT; US-PGPUB; EPO; JPO
20	BRS	9	((((370/465, 468,470,473,498,499,516-522,532,537,538. ccls.) and @ad<20010117) and delay and (timeslot (time near slot))) and (point-to-multipoint (point near (multi-point multipoint (multi adj point)))))) and TDM\$1) and (delay\$3 with compensat\$3)	USPAT; US-PGPUB; EPO; JPO
21	BRS	17	((370/465, 468,470,473,498,499,516-522,532,537,538. ccls.) and @ad<20010117) and delay and (timeslot (time near slot))) and (delay near compensat\$3)	USPAT; US-PGPUB; EPO; JPO
22	BRS	505	((370/465, 468,470,473,498,499,516-522,532,537,538. ccls.) and @ad<20010117) and delay and (timeslot (time near slot))	USPAT; US-PGPUB; EPO; JPO
23	BRS	8	((370/465, 468,470,473,498,499,516-522,532,537,538. ccls.) and @ad<20010117) and delay and (timeslot (time near slot)) and delay.ti.	USPAT; US-PGPUB; EPO; JPO
24	BRS	264	delay near compensat\$3.ti.	USPAT; US-PGPUB; EPO; JPO
25	BRS	201	(delay near compensat\$3.ti.) and @ad<20010117	USPAT; US-PGPUB; EPO; JPO
26	BRS	1	((delay near compensat\$3.ti.) and @ad<20010117) and timeslot.ti.	USPAT; US-PGPUB; EPO; JPO
27	BRS	486611	delay witht compensating with timeslot.ti.	USPAT; US-PGPUB; EPO; JPO
28	BRS	391246	(delay witht compensating with timeslot.ti.) and @ad<20010117	USPAT; US-PGPUB; EPO; JPO
29	BRS	2	delay with compensating with timeslot.ti.	USPAT; US-PGPUB; EPO; JPO
30	IS&R	1	("5570355").PN.	USPAT; US-PGPUB; EPO; JPO
31	BRS	0	(delay near compensat\$3.ti.) and @ad<20010117 and (second near control near logic)	USPAT; US-PGPUB; EPO; JPO
32	BRS	4	(delay near compensat\$3.ti.) and @ad<20010117 and (control\$3 near logic)	USPAT; US-PGPUB; EPO; JPO
33	BRS	14	(delay near compensat\$3.ti.) and @ad<20010117 and (control\$3 near second)	USPAT; US-PGPUB; EPO; JPO

	Time Stamp	Comments	Error Definition	Errors
19	2004/05/25 14:40			0
20	2004/05/25 14:40			0
21	2004/05/25 14:48			0
22	2004/05/25 15:08			0
23	2004/05/25 15:10			0
24	2004/05/25 15:10			0
25	2004/05/27 21:07			0
26	2004/05/25 15:27			0
27	2004/05/25 15:28			0
28	2004/05/25 15:28			0
29	2004/05/25 15:28			0
30	2004/05/27 15:22			0
31	2004/05/27 21:08			0
32	2004/05/27 21:11			0
33	2004/05/27 21:15			0

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5515371 A	19960507	14	Timeslot interleaving delay compensation (bonding) mechanism for time division multiplexed digital communication network	370/517
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5363373 A	19941108	6	Digital mobile station using presetable timeslot counter for compensating for propagation delay time	370/314
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6556585 B1	20030429	8	Method of controlling a communication system and the system employing the method	370/465
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6377590 B1	20020423	6	Apparatus and method for adaptive delay mitigation of voice communication	370/528
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20010050947	20011213	13	DELAY COMPENSATION	375/141
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20020136273	20020926	13	DELAY COMPENSATION	375/142
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6487235 B2	20021126	17	Delay compensation	375/130
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6408007 B1	20020618	6	Queue allocation control method for real time data transmission delay compensation of mobile communication system	370/412
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6377565 B1	20020423	13	Adaptive propagation delay compensation for TDMA communications systems	370/337
10	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6487222 B1	20021126	11	Quarter-rate 2B1Q ISDN architecture with embedded differential delay compensation for extending range of DDS communications	370/536
11	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5940403 A	19990817	11	Quarter-rate 2B1Q ISDN architecture with embedded differential delay compensation for extending range of DDS communications	370/465
12	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5226044 A	19930706	16	Low-rate encoding/digital speech interpolation system	370/435
13	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6731628 B1	20040504	38	Circuit-switched network	370/355
14	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6370125 B1	20020409	9	Dynamic delay compensation for packet-based voice network	370/312
15	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4761781 A	19880802	23	Adaptive packet/circuit switched transportation method and system	370/352

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed
1			Venters, W. Stuart	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5515371						
2	370/337; 370/350; 370/519; 375/358; 455/13.2; 455/67.16; 455/69		Nakahara, Kenji et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5363373						
3	370/347; 370/470		Moulsley, Timothy J.	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 6556585						
4	455/220; 704/226		Hoppal, Thomas Jake et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 6377590						
5			HOOLE, ELLIOT	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 20010050947						
6	375/144		HOOLE, ELLIOTT	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 20020136273						
7	375/316; 375/349		Hoole, Elliott	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 6487235						
8	370/230; 370/429; 370/468		Park, Jae-Hong	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 6408007						
9			Puckette, IV, Charles McDonald	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 6377565						
10	370/465; 370/477; 370/517; 370/522; 370/535		Williams, Philip David	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 6487222						
11	370/274; 370/492; 370/501; 370/536		Williams, Philip David	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5940403						
12	370/465; 370/476; 370/493		Gupta, Vaikunth N. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5226044						
13	370/352; 370/393		Shiomoto, Kohei et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 6731628						
14			Belk, R. Randall	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 6370125						
15	370/465		Calvignac, Jean et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 4761781						

	U	1	Document ID	Issue Date	Pages	Title	Current OR
16	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20010008533 A1	20010719	17	Delay-compensated timeslot assignment method and system for point-to-multipoint communication networks	370/468
17	<input type="checkbox"/>	<input checked="" type="checkbox"/>	JP 58218250 A	19831219	7	RESERVATION SYSTEM	
18	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5572517 A	19961105	25	Configurable hybrid medium access control for cable metropolitan area networks	370/431
19	<input type="checkbox"/>	<input checked="" type="checkbox"/>	JP 10242981 A	19980911	20	DYNAMIC TIME SLOT ASSIGNMENT SYSTEM	
20	<input type="checkbox"/>	<input checked="" type="checkbox"/>	JP 09214459 A	19970815	18	TDMA COMMUNICATION SYSTEM	
21	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5949781 A	19990907	14	Controller for ATM segmentation and reassembly	370/395.4
22	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5210750 A	19930511	18	Method and apparatus for distributed queue multiple access in a communication system	370/440
23	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 3721767 A	19730320	34	DELAY COMPENSATION IN MULTIPLEX TRANSMISSION SYSTEMS	370/435

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed
16	370/419		Umayabashi, Masaki	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20010008533					
17	370/349; 370/FOR.160		SHIKAMA, TOSHIHIRO	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 58218250 A					
18	370/433; 370/443; 370/449; 370/468; 370/470; 375/257; 725/1; 725/114; 725/119; 725/121; 725/131		Safadi, Reem	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5572517					
19			MIYABE, MASATAKE et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 10242981 A					
20			UMEUCHI, MAKOTO et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 09214459 A					
21	370/468		Lincoln, Bradford C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5949781					
22	340/825.5		Nassehi, Mehdi M. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5210750					
23	455/12.1; 455/69		LaMarche, Robert Ernest et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 3721767					